



By using A0-A3 4 bits we get #0-F A4,5&6 decode these from the the original D3xx range to 8 x #F addresses. this could be altered to 8x #1F addresses by using A5,6,7 or another decoder alongside the additional one's giving the High block of addresses that are missing ie D380-D3FF

By Simply_Graham	
Sheet: /	
File: Decoder D3XX.sch	
Title: Additional Address decoding	
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